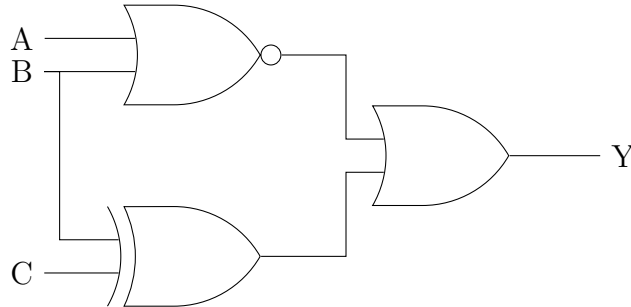


1. 2019-2020 Examples handout questions 1-8.
2. Write the truth table for this circuit:



3. Give a boolean logic expression in sum-of-products (DNF) form where the minimal representation is made up of the following:
 - (a) Four essential prime implicants of 4 literals.
 - (b) An essential prime implicant of 2 literals, and one of 4 literals.
4. Show how to implement the boolean function F using only NAND gates:
$$F(A, B, C, D, X, Y) = (A \cdot B) + (C \cdot D) + (X \cdot Y)$$